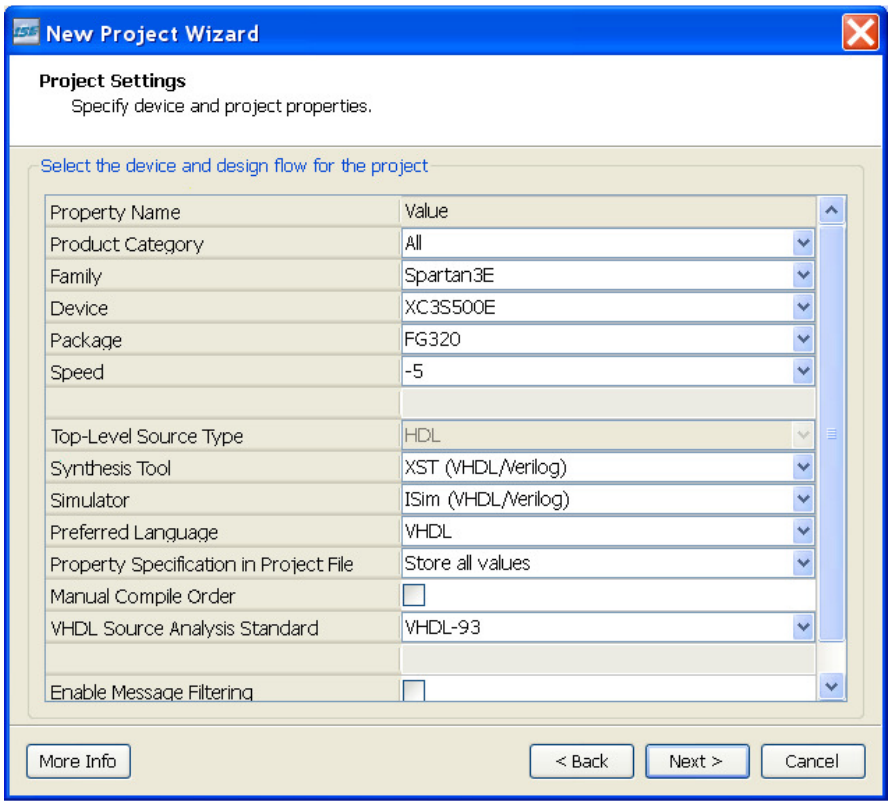
Device and Design Flow Setup

Family: Spartan3E
Device: XC3S500E or XC3S1200E depending on the board
Package: FG320



The above example was created for the XC3S500E chip which has 500K gates. If the FPGA chip on your board is XC3S1200E (1200K gate version), you must set the device to XC3S1200E.