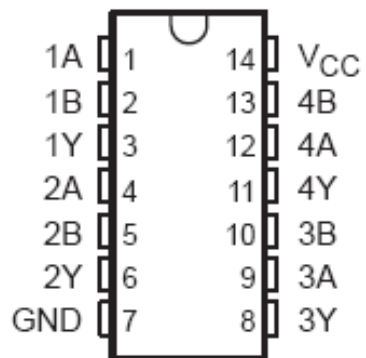


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Quad 2-input Positive NAND Gates



Positive Logic: NAND

$$Y = \overline{AB} = (AB)' = A' + B'$$

