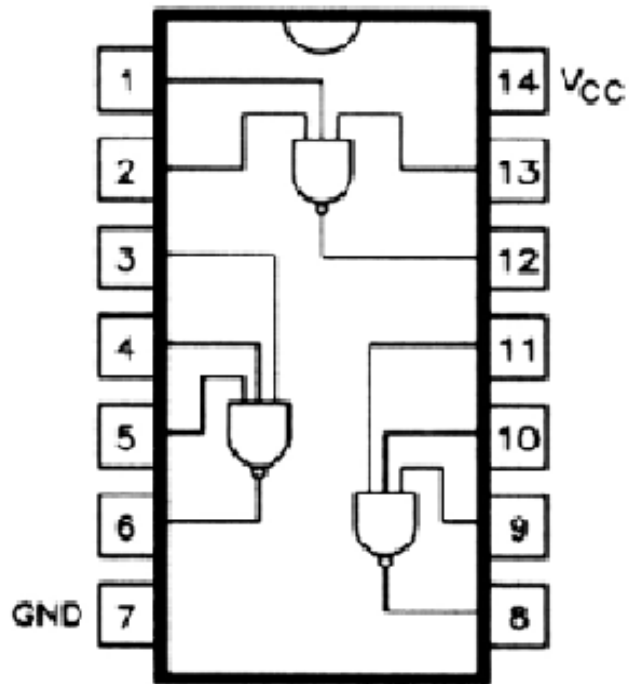


74LS10

Triple 3-Input NAND Gates



Positive Logic

$$Y = \overline{ABC} = (ABC)'$$