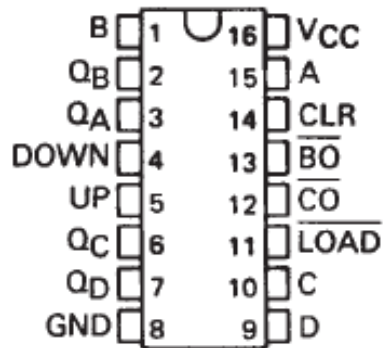
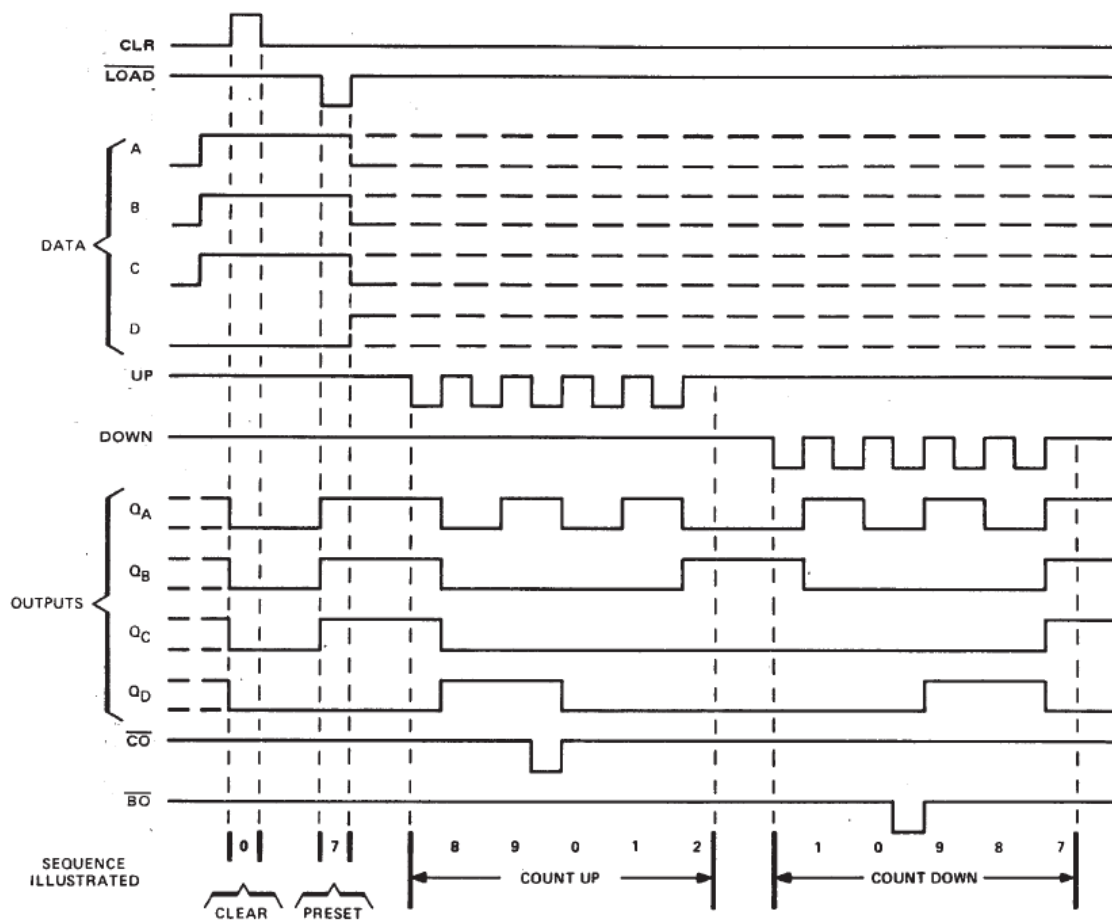


74LS192

SYNCHRONOUS 4-BIT BCD UP/DOWN COUNTER



Timing Diagram



NOTES: A. Clear overrides load, data, and count inputs.
B. When counting up, count-down input must be high; when counting down, count-up input must be high.

Source: Texas Instruments TTL Data Book