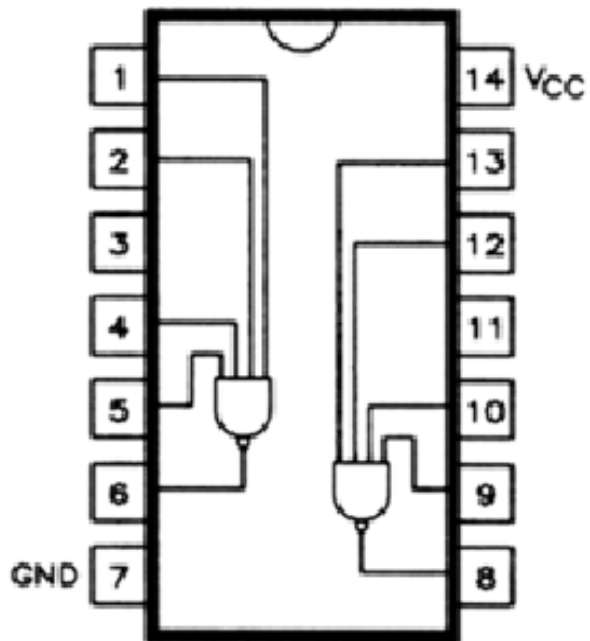


74LS20

Dual 4-Input NAND Gates



Positive Logic: 4-input NAND

$$Y = \overline{ABCD} = (ABCD)' = A' + B' + C' + D'$$